

-	87	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) not (((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and ((710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.) or (710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.))) not (((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and (711/105.ccls. or 711/100.ccls. or 711/212.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:45
-	678	711/5.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 10:01
-	122426	memory same (controller or interface or bridge) same (microprocessor or cpu or processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 09:59
-	338	711/5.ccls. and (memory same (controller or interface or bridge) same (microprocessor or cpu or processor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/16 18:33
-	30169	memory same (controller or interface or bridge) same "9"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 10:00
-	122623	memory same (controller or interface or bridge) same (microprocessor or cpu or processor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 10:35
-	43137	memory same (controller or interface or bridge) same (buffer or queue or fifo)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 11:07
-	678	711/5.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 10:02
-	156	(memory same (controller or interface or bridge) same (microprocessor or cpu or processor)) and (memory same (controller or interface or bridge) same (buffer or queue or fifo)) and 711/5.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 10:02

-	26021	memory same (controller or interface or bridge) same port	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 10:03
-	52	((memory same (controller or interface or bridge) same (microprocessor or cpu or processor)) and (memory same (controller or interface or bridge) same (buffer or queue or fifo)) and 711/5.ccls.) and (memory same (controller or interface or bridge) same port)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 10:04
-	51	((memory same (controller or interface or bridge) same (microprocessor or cpu or processor)) and (memory same (controller or interface or bridge) same (buffer or queue or fifo)) and 711/5.ccls.) and (memory same (controller or interface or bridge) same port)) and (@ad<19990929 or @rlad<19990929)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 11:08
-	21284	(ic or integrated or chip or asic) same (memory same (controller or interface or bridge) same (microprocessor or cpu or processor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 10:34
-	12864	((interfac\$3 or bridge or controller) near6 (chip or asic or integrated)) same memory	USPAT; US-PGPUB	2002/12/17 10:36
-	31	((memory same (controller or interface or bridge) same (microprocessor or cpu or processor)) and (memory same (controller or interface or bridge) same (buffer or queue or fifo)) and 711/5.ccls.) and (memory same (controller or interface or bridge) same port)) and (@ad<19990929 or @rlad<19990929)) and ((ic or integrated or chip or asic) same (memory same (controller or interface or bridge) same (microprocessor or cpu or processor)))	USPAT; US-PGPUB	2002/12/17 11:05
-	763	711/104-105.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 11:05
-	43137	memory same (controller or interface or bridge) same (buffer or queue or fifo)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 11:07
-	45	(memory same (controller or interface or bridge) same (microprocessor or cpu or processor)) and (memory same (controller or interface or bridge) same port) and ((interfac\$3 or bridge or controller) near6 (chip or asic or integrated)) same memory) and (memory same (controller or interface or bridge) same (buffer or queue or fifo)) and 711/104-105.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 11:08
-	42	((memory same (controller or interface or bridge) same (microprocessor or cpu or processor)) and (memory same (controller or interface or bridge) same port) and ((interfac\$3 or bridge or controller) near6 (chip or asic or integrated)) same memory) and (memory same (controller or interface or bridge) same (buffer or queue or fifo)) and 711/104-105.ccls.) and (@ad<19990929 or @rlad<19990929)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/17 11:08